

FIG. 1



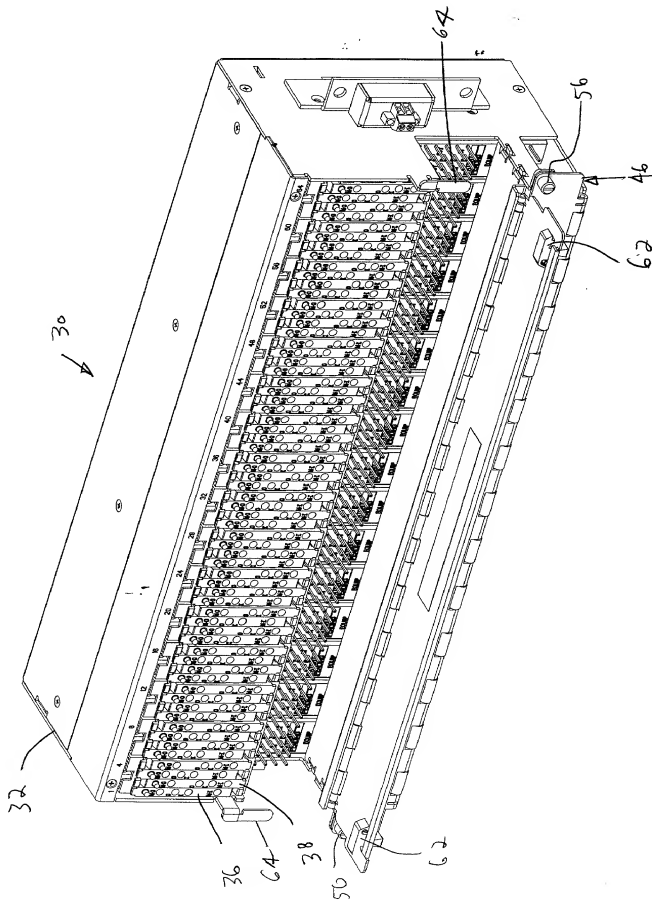
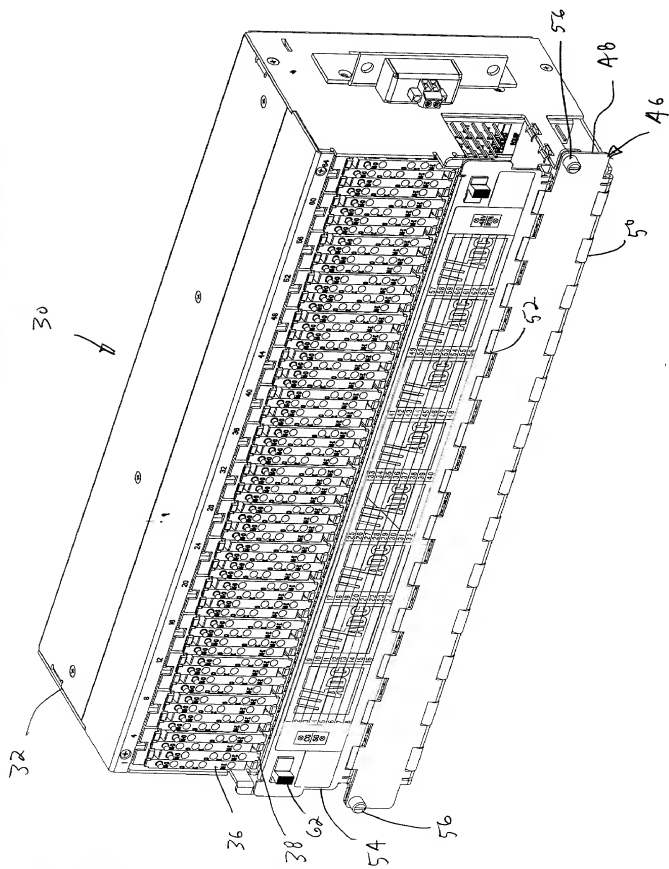


FIG. 2B

00260-91279660

00260*91E29660



F16. 2C

09967346-0992601

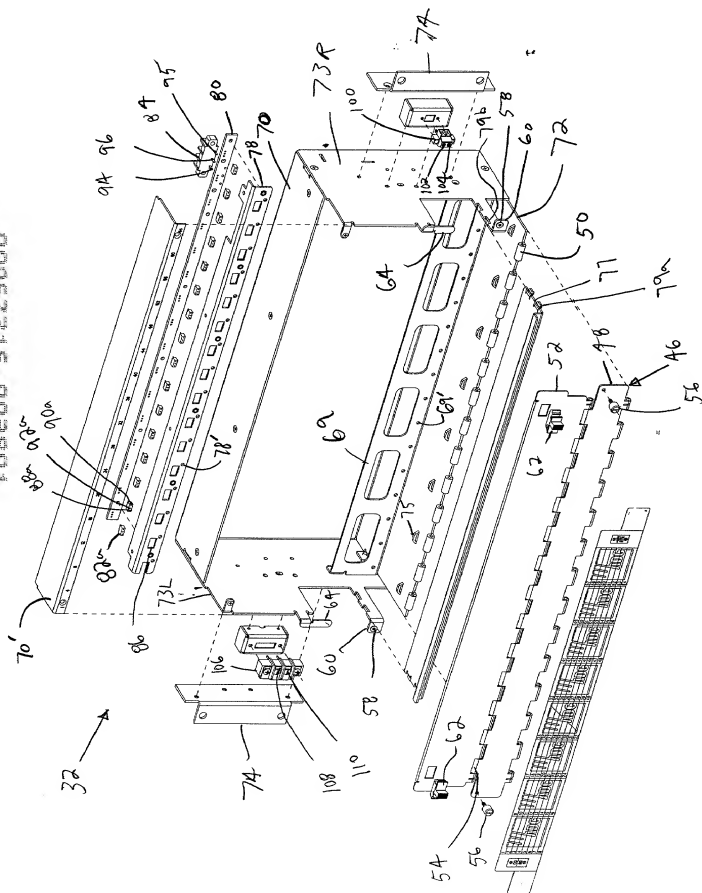


FIG 3

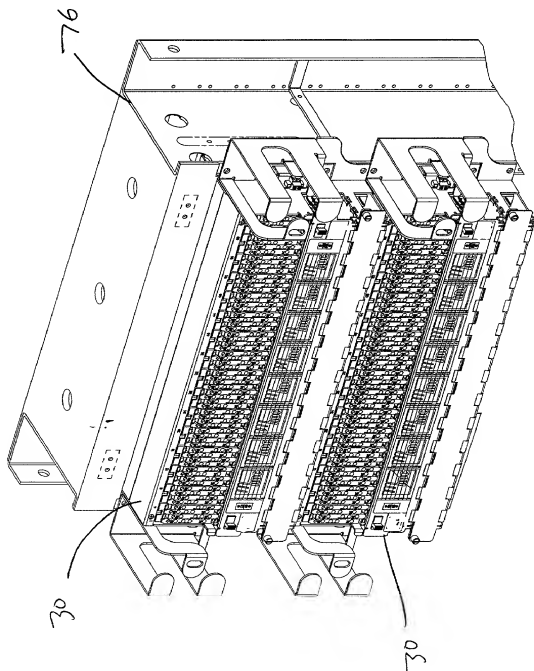


FIG. 4

09667316.002001

108260-91329660

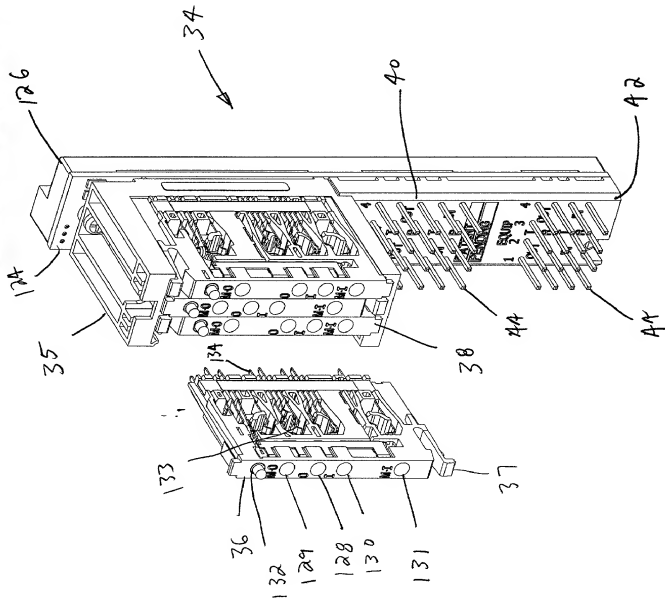


FIG. 5A



100260-91E79660

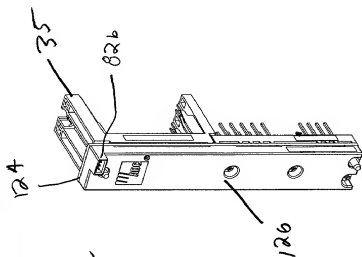


FIG. 5D

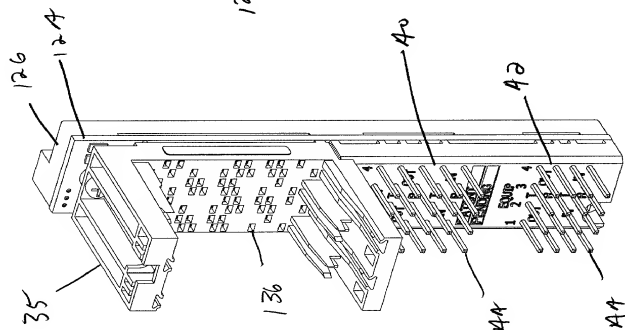


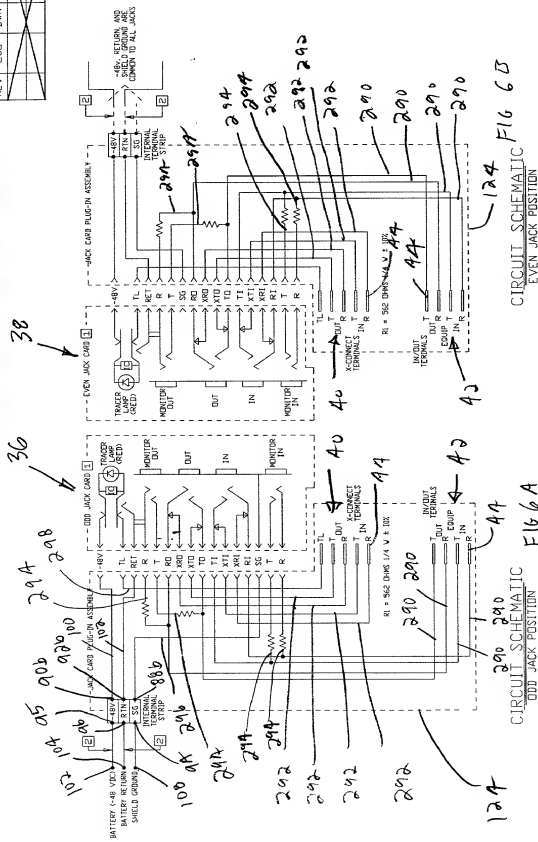
FIG. 5C

0967318-09604

4

B

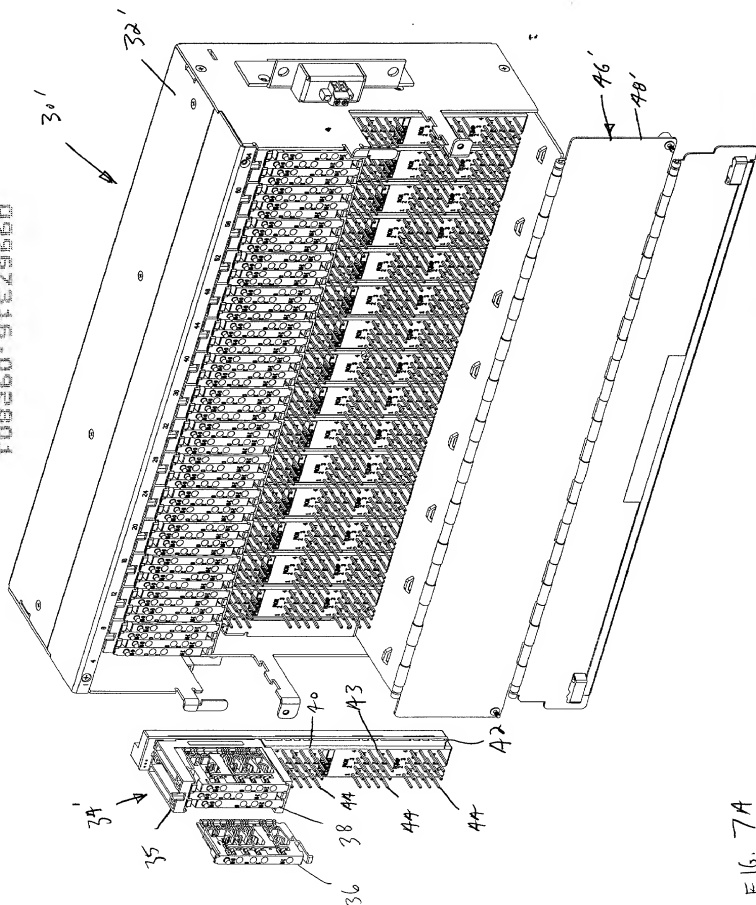
REV	ECD	DWN	DATE



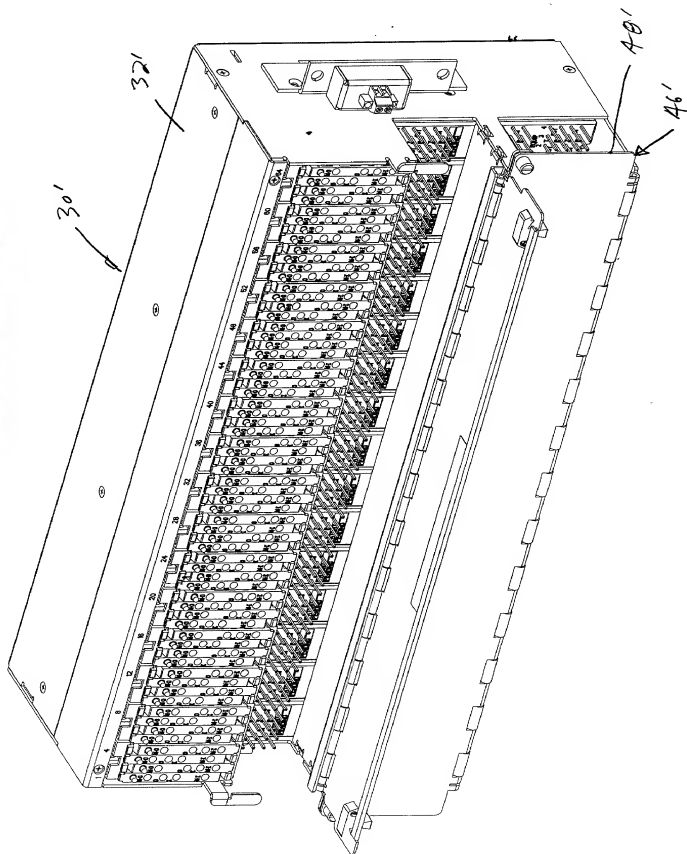
290 240
CIRCUIT SCHEMATIC
000 JACK POSITION

124
CIRCUIT SCHEMATIC
EVEN JACK POSITION
FIG 6B

09967316.092801



09967316.092R01



F16.7B

09967316.092801

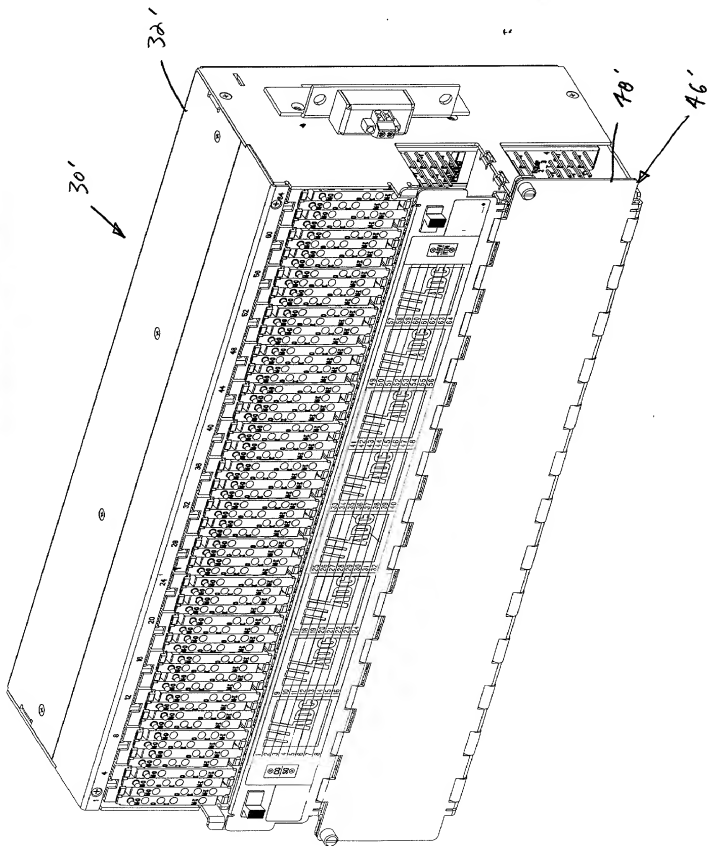


FIG. 7C

09967316-092801

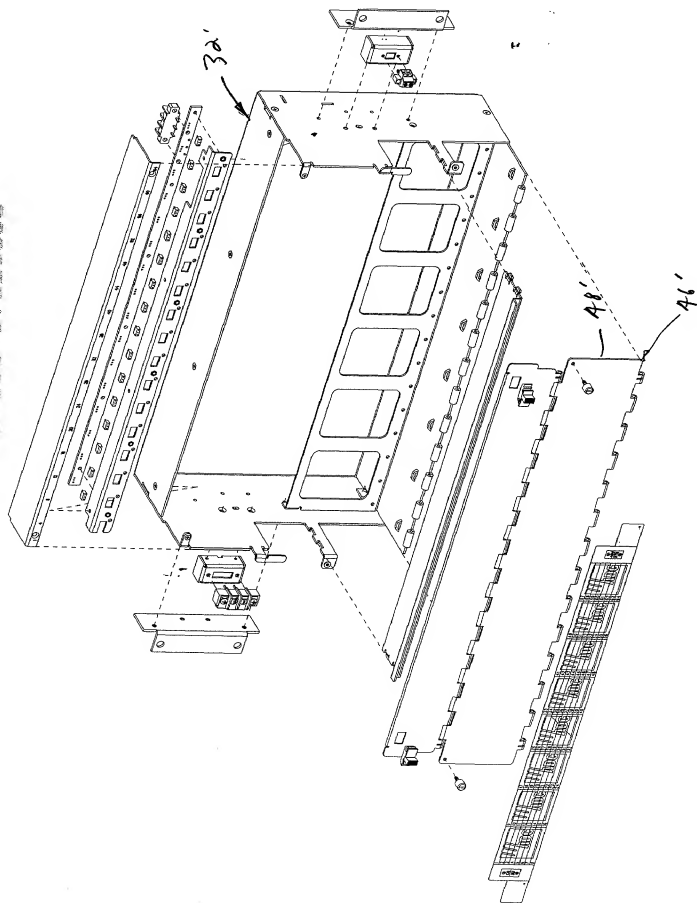
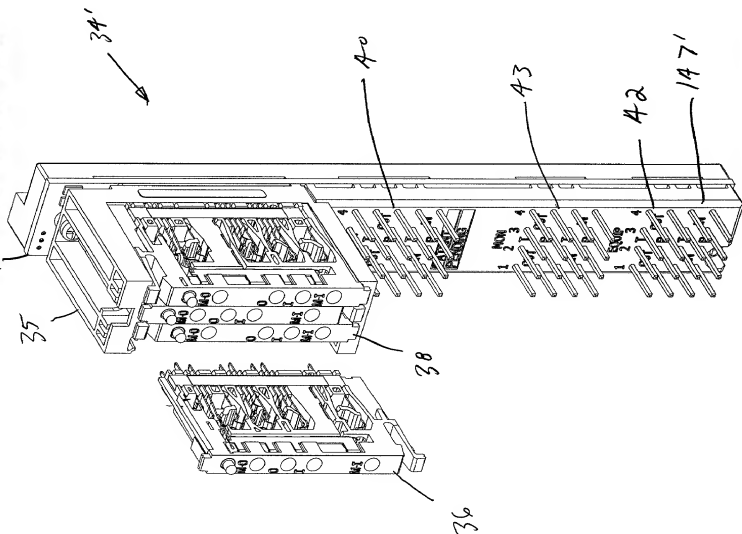
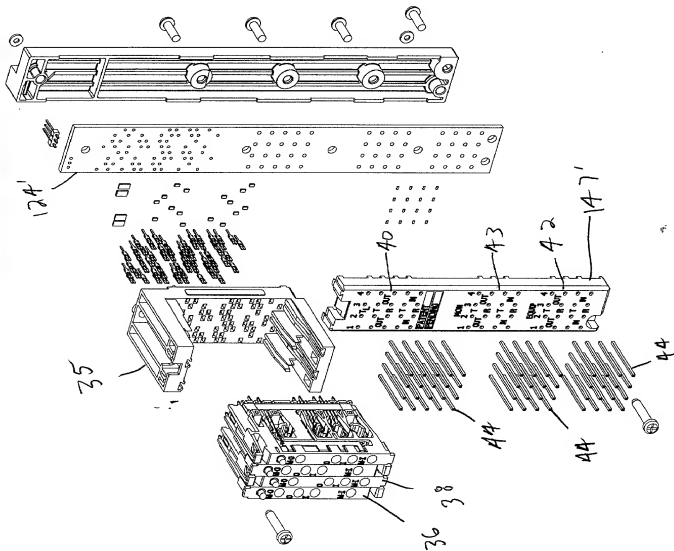


FIG. 8

092660 91E29660



092601 092601 092601



108260-91E/9660

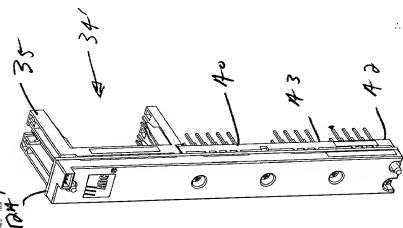


FIG. 9D

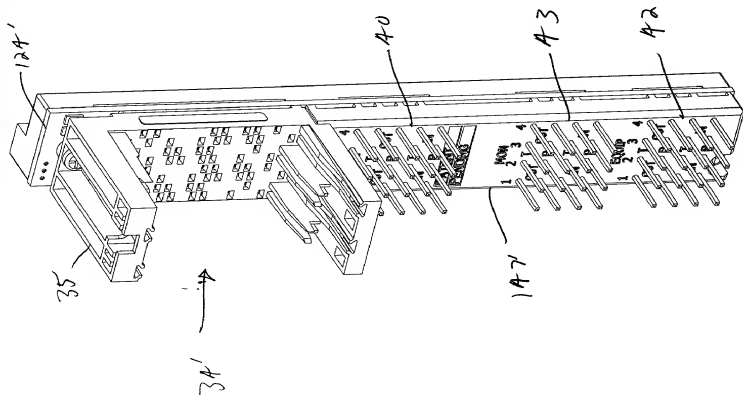


FIG. 9C



F-16. 10A

CIRCUIT SCHEMATIC EVEN JACK POSITION

CIRCUIT SCHEMATIC ODD JACK POSITION